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METHOD AND APPARATUS FOR ENSURING INTEGRITY
OF CRITICAL RAM VARIABLES

ABSTRACT

5 Methods and apparatus are provided for ensuring the integrity of a software variable stored in memory. The method comprises the steps of verifying the integrity of the specific hardware to be used in calculating and storing software variables, calculating a software variable and a dual store software variable using the verified hardware, storing both software variables in verified memory, and comparing the two stored software variables to verify the integrity of the software used for calculating and storing the two software variables.